

FIG. 1

MPU PRIVATE PERIPHERAL ~ 142

DSP PRIVATE PERIPHERAL ~ 140

FIG. 2A

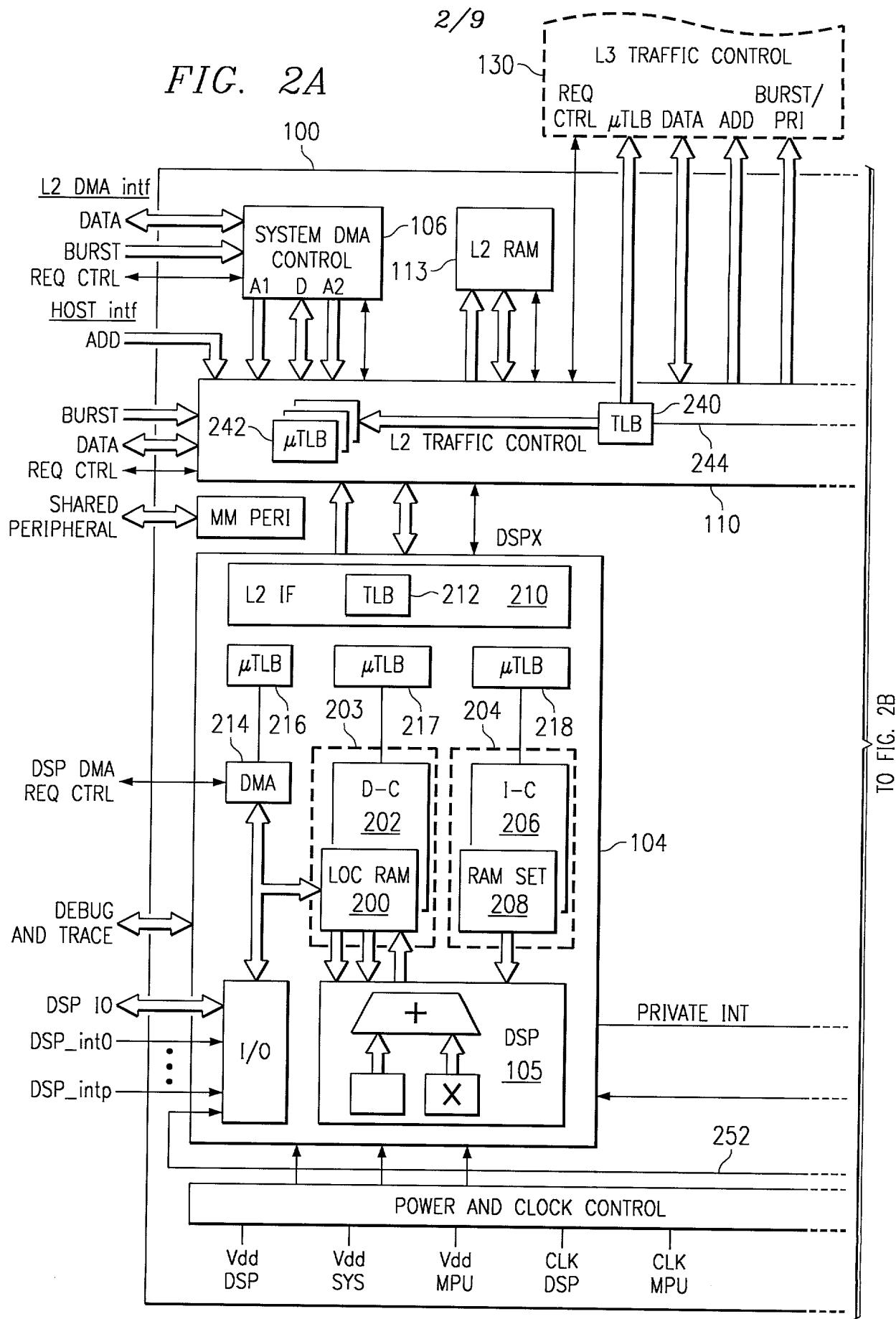
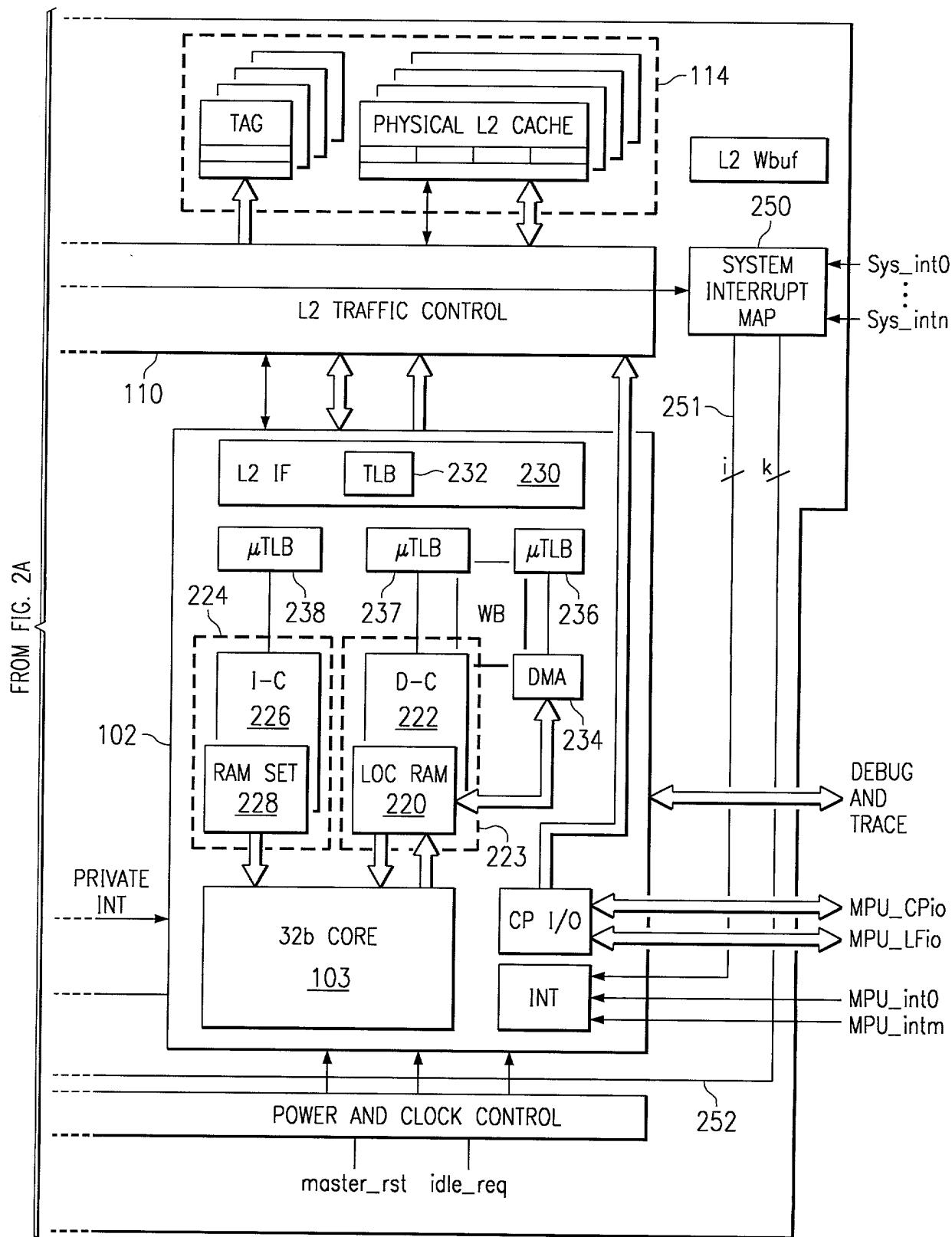


FIG. 2B



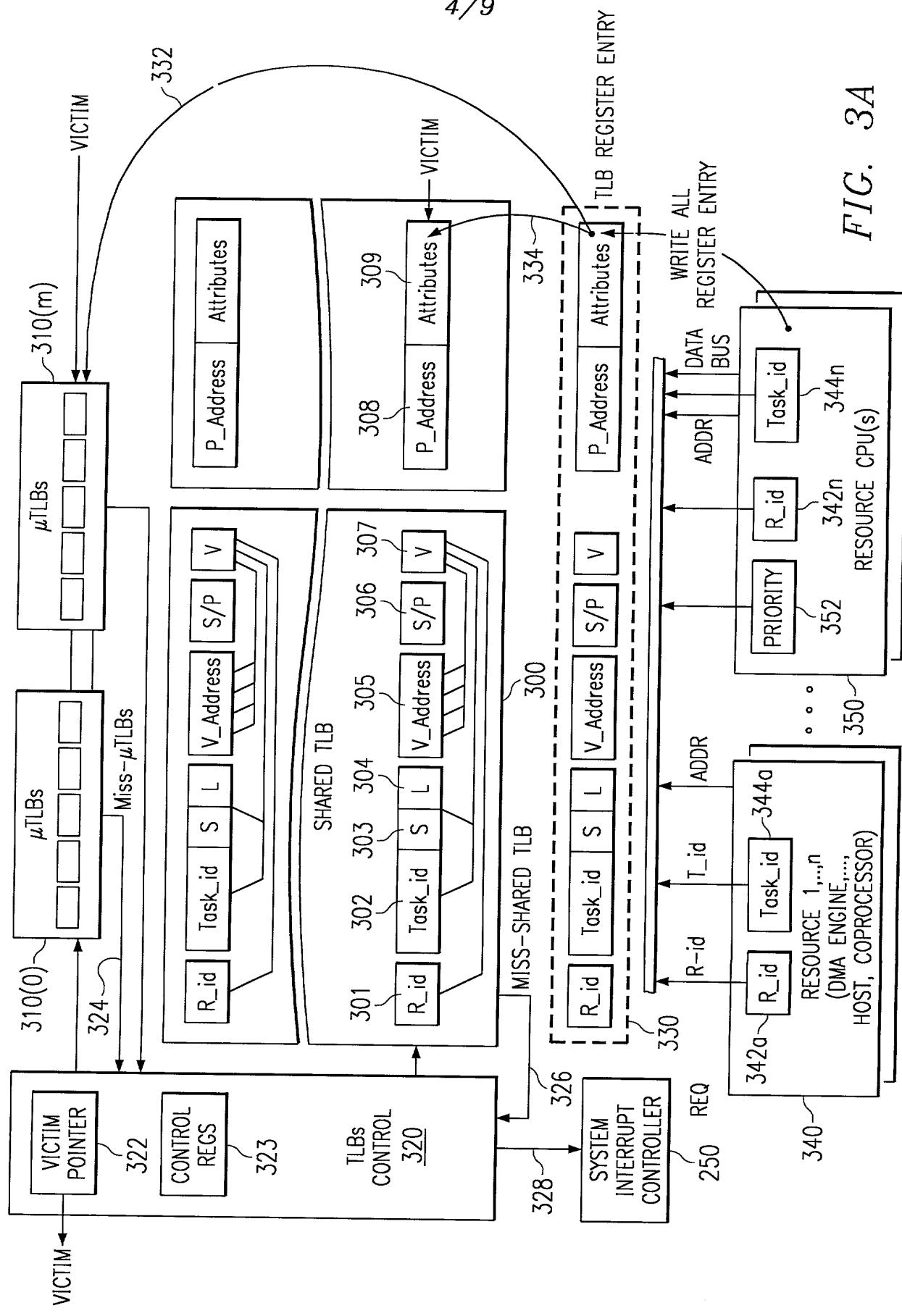
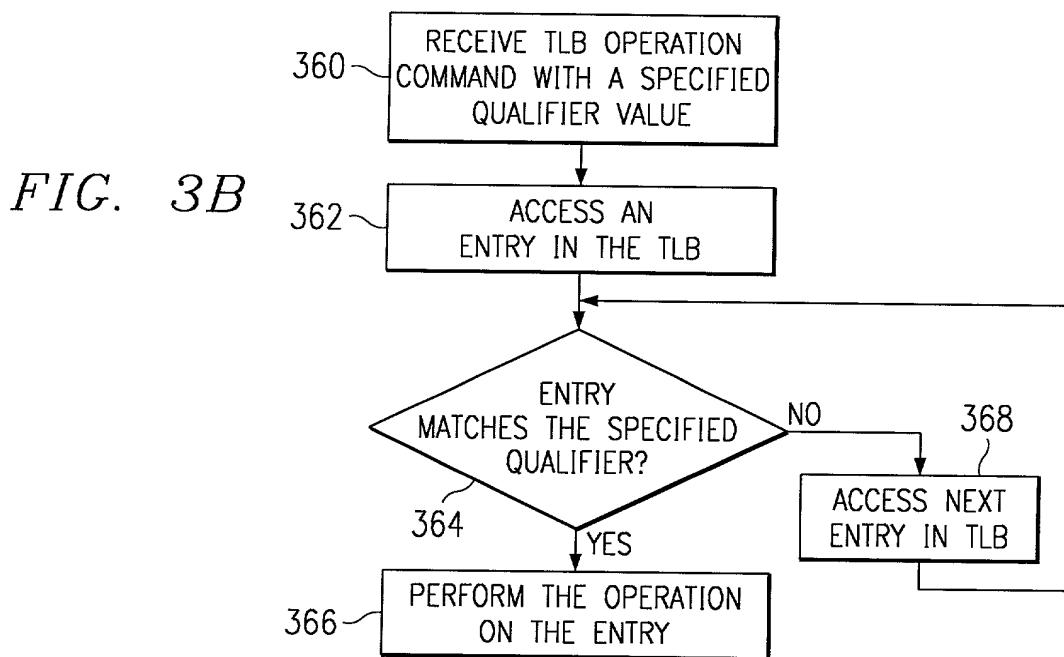
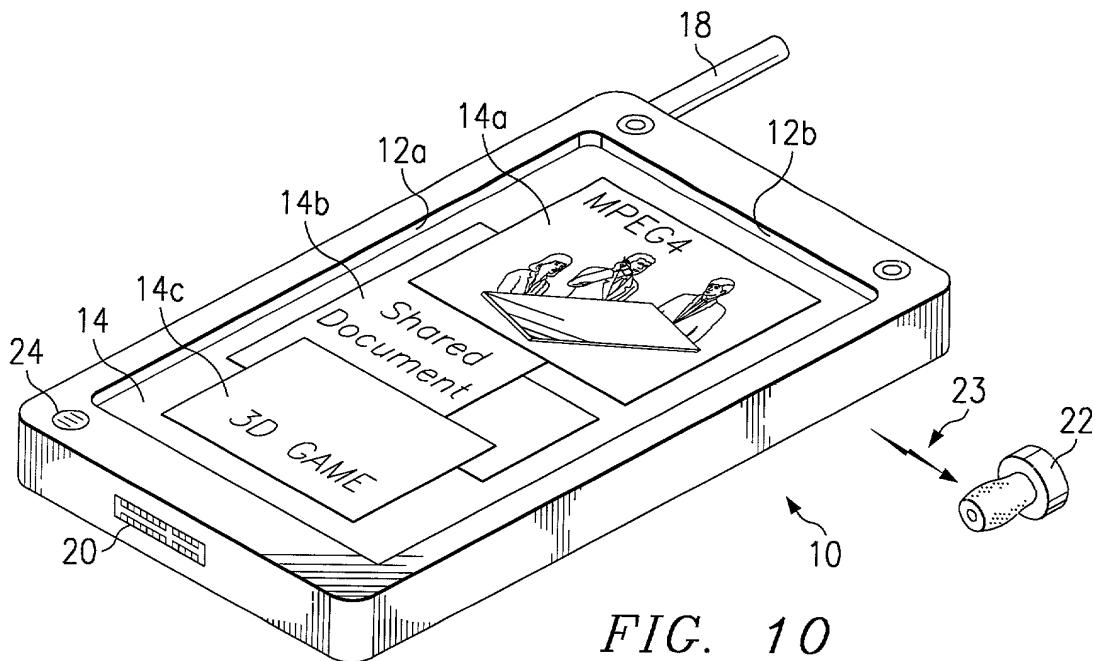
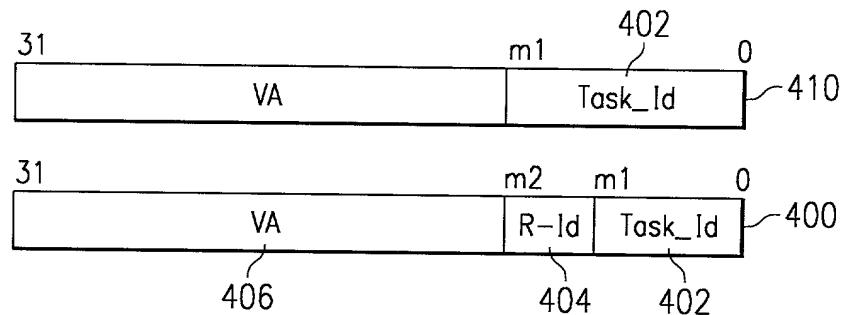


FIG. 34

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*FIG. 5*

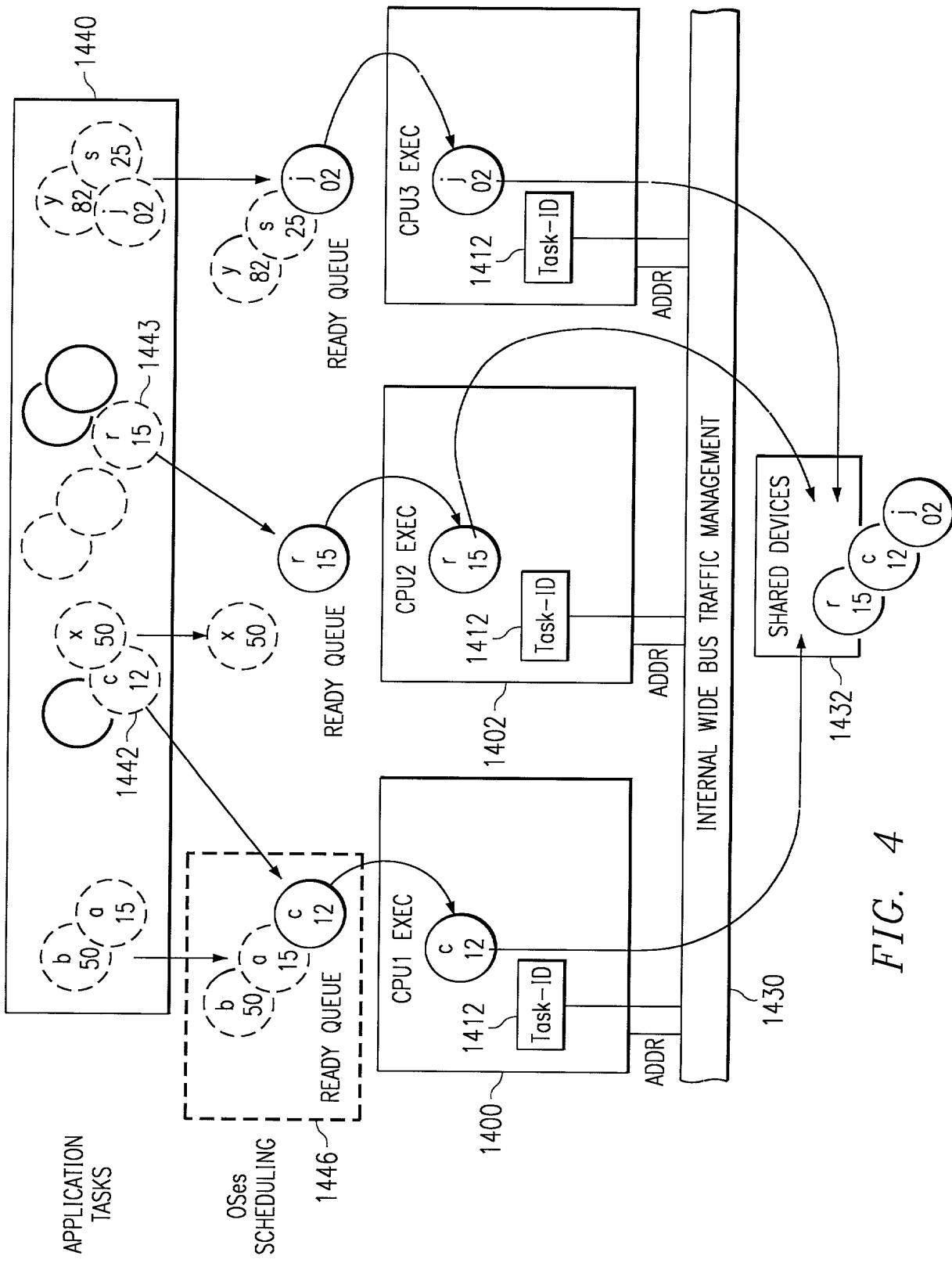


FIG. 4

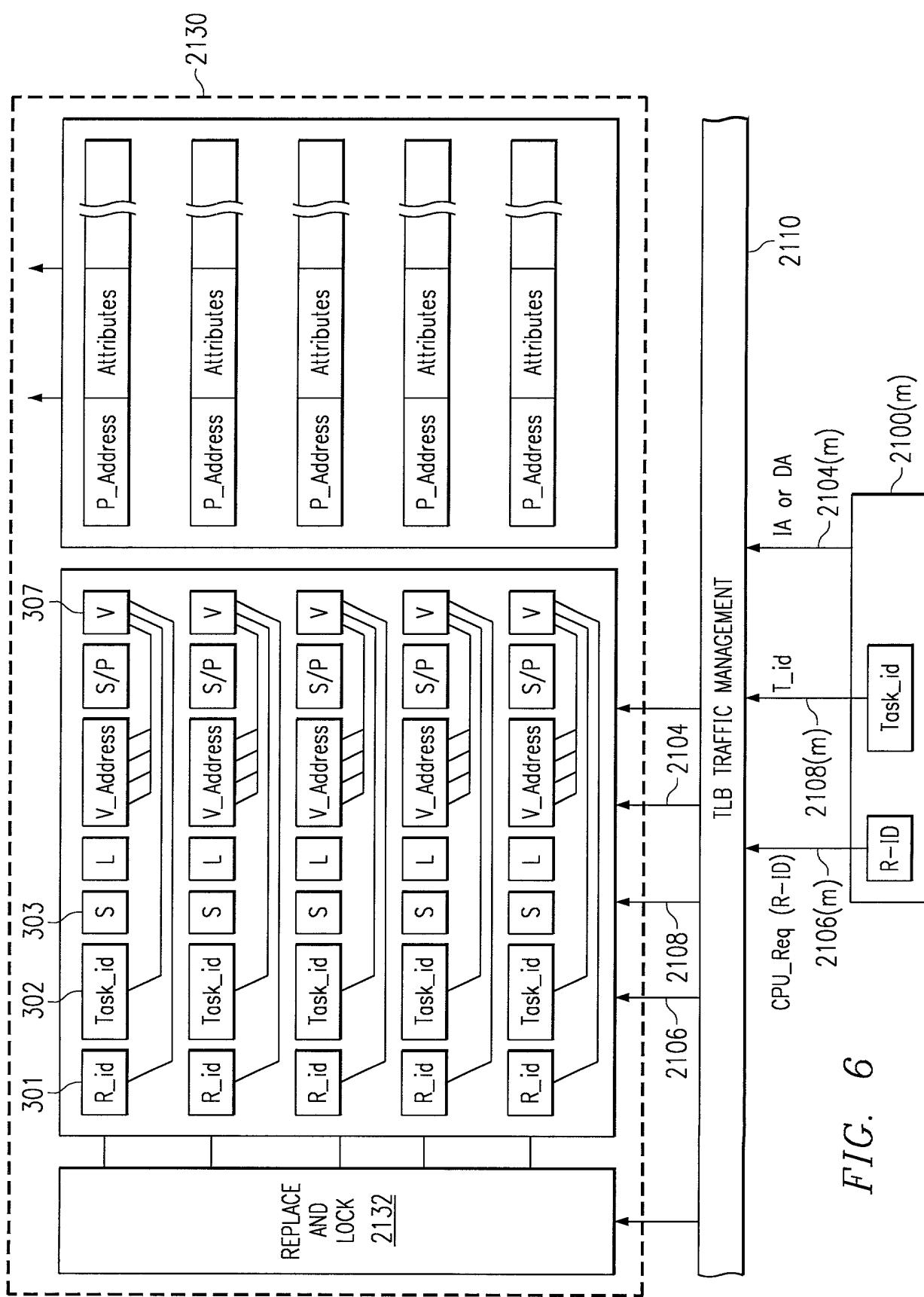


FIG. 6

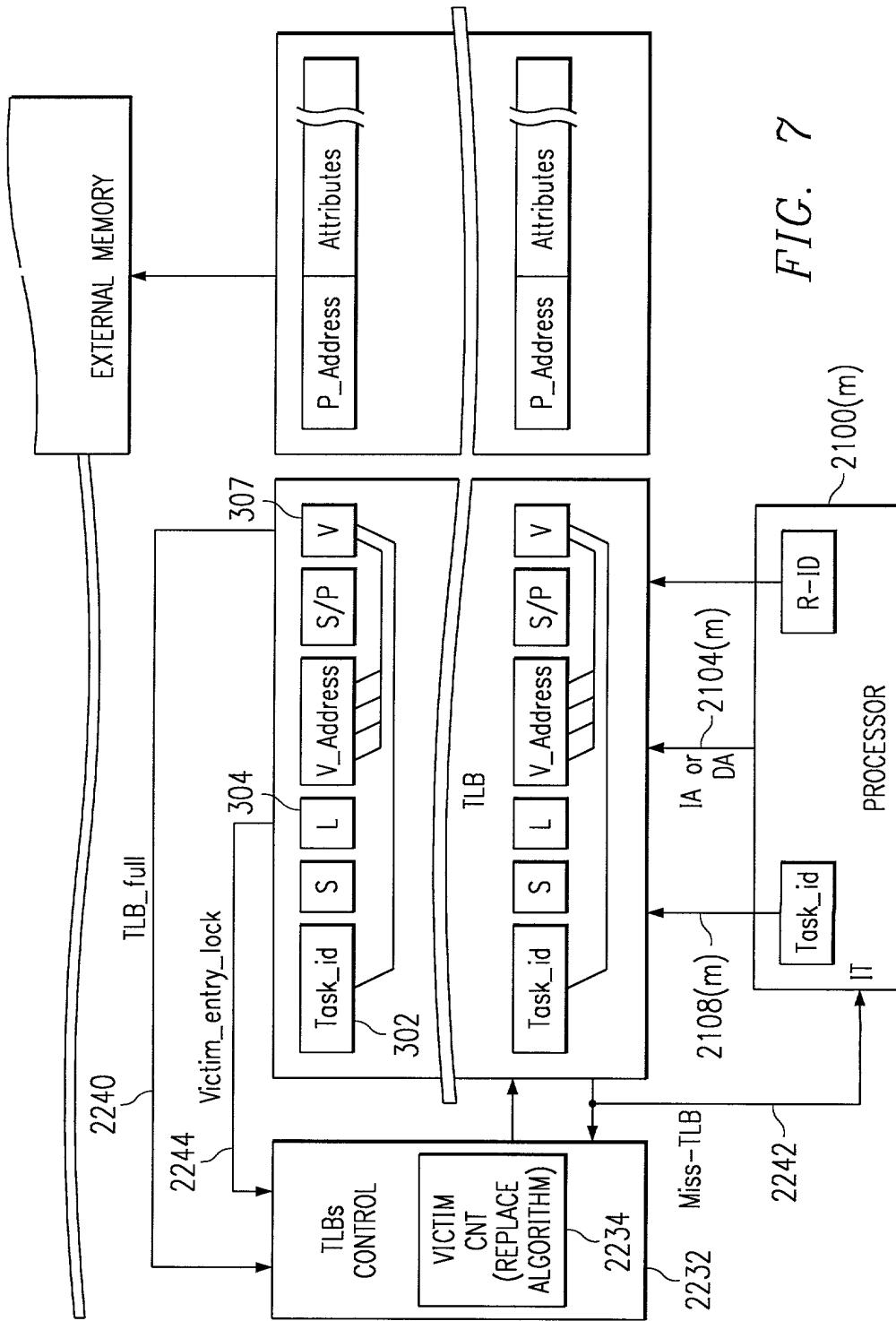


FIG. 7

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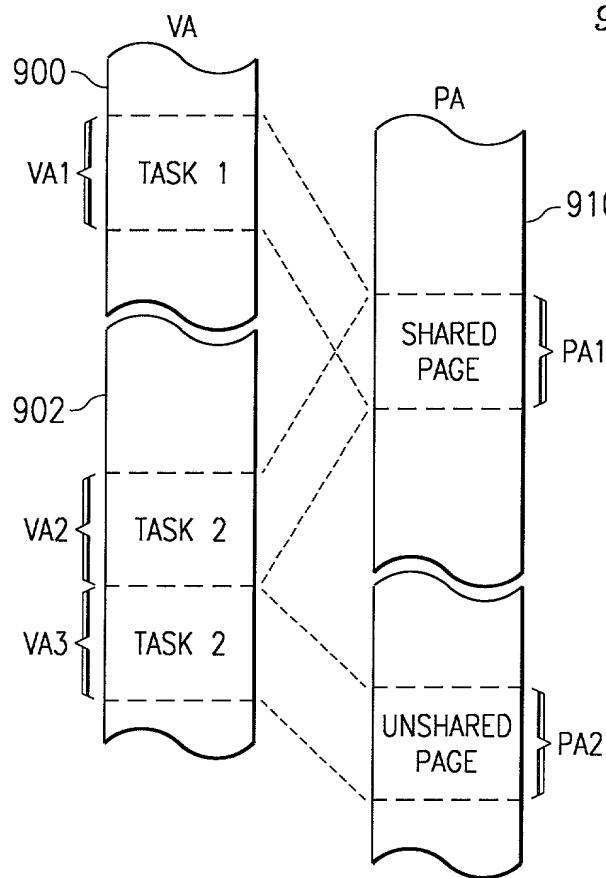


FIG. 8

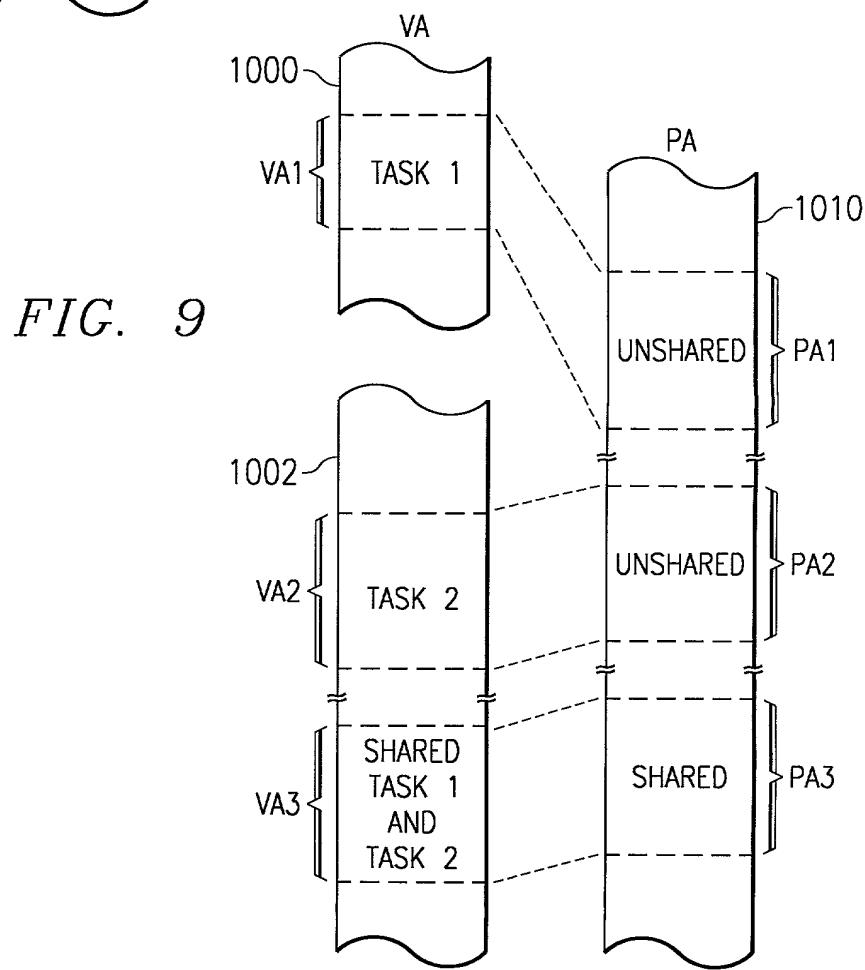


FIG. 9